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Doc description: Information Disclosure Statement (IDS) Filed

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10033027
	Filing Date		2001-10-22
	First Named Inventor	Warren Snyder	
	Art Unit	2183	
	Examiner Name	Idriss N. Airoyaye	
	Attorney Docket Number	CD00232M	

**U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
	7	6823282	B1	2004-11-23	Snyder	Entire Document
	8	7127630	B1	2006-10-24	Snyder	Entire Document

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9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
13	6952778	B1	2005-10-04	Snyder	Entire Document
14	7185162	B1	2007-02-27	Snyder	Entire Document
15	6854067	B1	2005-02-08	Kutz et al.	Entire Document
16	6614320	B1	2003-09-02	Sullam et al.	Entire Document
17	6868500	B1	2005-03-15	Kutz et al.	Entire Document
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19	7149316	B1	2006-12-12	Kutz et al.	Entire Document

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21	6603330	B1	2003-08-05	Snyder	Entire Document
22	6507214	B1	2003-01-14	Snyder	Entire Document
23	7005933	B1	2006-02-28	Shutt	Entire Document
24	6542025	B1	2003-04-01	Kutz et al.	Entire Document
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26	6957242	B1	2005-10-18	Snyder	Entire Document
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31	6563391	B1	2003-05-13	Mar	Entire Document
32	6608472	B1	2003-08-19	Kutz et al.	Entire Document
33	7185321	B1	2007-02-27	Roe et al.	Entire Document

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**U.S. PATENT APPLICATION PUBLICATIONS**

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**FOREIGN PATENT DOCUMENTS**

Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>

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2	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 06/30/08; 12 pages	<input type="checkbox"/>
3	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 12/26/07; 12 pages	<input type="checkbox"/>
4	USPTO Final Rejection for 10/238,966 (CD01250) dated 09/27/07; 9 pages	<input type="checkbox"/>
5	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/19/07; 7 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 10/20/06; 8 pages	<input type="checkbox"/>
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8	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al.; 25 pages	<input type="checkbox"/>
9	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Mar; 28 pages	<input type="checkbox"/>
10	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance," Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
11	"WP 3.5: An Integrated Time Reference," Blauschild, Digest of Technical Papers, 1994; 4 pages	<input type="checkbox"/>
12	"Micropower CMOS Temperature Sensor with Digital Output," Bakker et al., IEEE Journal of Solid-State Circuits, 1996; 3 pages	<input type="checkbox"/>

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13	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 05/11/05; 10 pages	<input type="checkbox"/>
14	USPTO Final Rejection for 10/001,477 (CD01203) dated 10/24/05; 13 pages	<input type="checkbox"/>
15	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/06; 14 pages	<input type="checkbox"/>
16	USPTO Final Rejection for 10/001,477 (CD01203) dated 08/24/06; 13 pages	<input type="checkbox"/>
17	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 10/20/08; 18 pages	<input type="checkbox"/>
18	USPTO Final Rejection for 10/001,478 (CD01213) dated 06/04/08; 18 pages	<input type="checkbox"/>
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24	USPTO Final Rejection for 10/001,478 (CD01213) dated 10/24/05; 15 pages	<input type="checkbox"/>
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27	USPTO Notice of Allowance for 10/002,217 (CD01207) dated 10/14/08; 6 pages	<input type="checkbox"/>
28	USPTO Notice of Allowance for 10/002,217 (CD01207) dated 06/06/08; 7 pages	<input type="checkbox"/>
29	USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages	<input type="checkbox"/>
30	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 08/03/07; 10 pages	<input type="checkbox"/>
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32	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 10/02/06; 21 pages	<input type="checkbox"/>
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35	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 05/19/05; 15 pages	<input type="checkbox"/>
36	USPTO Notice of Allowance for 10/001,477 (CD01203) dated 11/10/08; 7 pages	<input type="checkbox"/>
37	USPTO Advisory Action for 10/001,477 (CD01203) dated 10/10/08; 3 pages	<input type="checkbox"/>
38	USPTO Final Rejection for 10/001,477 (CD01203) dated 06/30/08; 15 pages	<input type="checkbox"/>
39	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 12/06/07; 13 pages	<input type="checkbox"/>
40	USPTO Final Rejection for 10/001,477 (CD01203) dated 07/23/07; 16 pages	<input type="checkbox"/>
41	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 01/22/07; 15 pages	<input type="checkbox"/>

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**EXAMINER SIGNATURE**

Examiner Signature	/Idriss Alrobaye/	Date Considered	08/22/2009
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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## U.S.PATENTS

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	1	5982229	B1	1999-11-09	Wong et al.	Entire Document
	2	6535948	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
	4	5594734	B1	1997-01-14	Worsley et al.	Entire Document
	5	5781747	B1	1998-07-14	Kametani	Entire Document
	6	6604179	B1	2003-08-05	Volk et al.	Entire Document
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9	6249167	B1	2001-06-19	Oguchi et al.	Entire Document
10	4134073	B1	1979-01-01	MacGregor	Entire Document
11	5557762	B1	1996-09-17	Okuaki et al.	Entire Document
12	6771552	B1	2004-08-03	Fujisawa	Entire Document
13	5371860	B1	1994-12-06	Mura et al.	Entire Document
14	6575373	B1	2003-06-10	Nakano	Entire Document
15	5710906	B1	1998-01-20	Ghosh et al.	Entire Document
16	6294962	B1	2001-09-25	Mar	Entire Document
17	5590354	B1	1996-12-31	Klapproth et al.	Entire Document
18	5630102	B1	1997-05-13	Johnson et al.	Entire Document
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20	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
21	5552748	B1	1996-09-03	O'Shaughnessy	Entire Document
22	4689740	B1	1987-08-25	Moelands et al.	Entire Document
23	5559502	B1	1996-09-24	Schutte	Entire Document
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25	5670915	B1	1997-09-23	Cooper et al.	Entire Document
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31	5428319	B1	1995-06-27	Marvin et al.	Entire Document
32	5604466	B1	1997-02-18	Dreps et al.	Entire Document
33	5898345	B1	1999-04-27	Namura et al.	Entire Document
34	5304955	B1	1994-04-19	Atriss et al.	Entire Document
35	5870345	B1	1999-02-09	Stecker	Entire Document
36	5870004	B1	1999-02-09	Lu	Entire Document
37	5699024	B1	1997-12-16	Manlove et al.	Entire Document
38	5345195	B1	1994-09-06	Cordoba et al.	Entire Document
39	4272760	B1	1981-06-09	Prazak et al.	Entire Document
40	4344067	B1	1982-08-10	Lee	Entire Document
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42	4947169	B1	1990-08-07	Smith et al.	Entire Document
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47	5440305	B1	1995-08-08	Signore et al.	Entire Document
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**U.S. PATENT APPLICATION PUBLICATIONS**

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	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
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**NON-PATENT LITERATURE DOCUMENTS**

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	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
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Application Number		10033027
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First Named Inventor	Warren Snyder	
Art Unit	2183	
Examiner Name	Idriss N. Alrobaye	
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	1	"Pod-Wikipedia, the free encyclopedia"; retrieved on November 14, 2005 from <a href="http://en.wikipedia.org/wiki/Pod">http://en.wikipedia.org/wiki/Pod</a> ; 3 pages	<input type="checkbox"/>
	2	"pod-defintion by dict.die.net"; retrieved on November 14, 2005 from <a href="http://dict.die.net/pod">http://dict.die.net/pod</a> ; 2 pages	<input type="checkbox"/>
	3	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2005 from <a href="http://www.algonet.se/~staffann/developer/emulator.htm">http://www.algonet.se/~staffann/developer/emulator.htm</a> ; 6 pages	<input type="checkbox"/>
	4	CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation," October 10, 2001; 09/975,104; Snyder; 35 pages	<input type="checkbox"/>
	5	USPTO Non-Final Rejection for 09/989,767 (CD01178M) dated 07/24/08; 21 pages	<input type="checkbox"/>
	6	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	<input type="checkbox"/>
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

8	Microsoft Press Computer User's Dictionary; 1998; 3 pages	<input type="checkbox"/>
9	Sreeram Duvvuru and Siamak Arya, "Evaluation of a Branch Target Address Cache," 1995; IEEE; 8 pages	<input type="checkbox"/>
10	Andrew S. Tanenbaum with contributions from James R. Goodman, "Structured Computer Organization," 1999, Prentice Hall, Fourth Edition; 32 pages	<input type="checkbox"/>
11	CD00186; "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events;" October 10, 2001; 09/975,338; Nemecek et al.; 34 pages	<input type="checkbox"/>
12	CD00185; "Emulator Chip-Board Architecture for Interface;" October 10, 2001; 09/975,030; Snyder et al.; 37 pages	<input type="checkbox"/>
13	Wikipedia - Main Page, retrieved on March 8, 2006 from <a href="http://en.wikipedia.org/wiki/Main_Page">http://en.wikipedia.org/wiki/Main_Page</a> and <a href="http://en.wikipedia.org/wiki/Wikipedia:Introduction">http://en.wikipedia.org/wiki/Wikipedia:Introduction</a> ; 5 pages	<input type="checkbox"/>
14	Wikipedia - Processor register, retrieved on March 7, 2006 from <a href="http://en.wikipedia.org/wiki/Processor_register">http://en.wikipedia.org/wiki/Processor_register</a> ; 4 pages	<input type="checkbox"/>
15	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 259 pages	<input type="checkbox"/>
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18	CD00184; "Host to FPGA Interface in an In-Circuit Emulation System;" 10/10/01; 09/975,105; Nemecek; 44 pages	<input type="checkbox"/>

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### U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	7188063	B1	2007-03-06	Snyder, Warren	Entire Document
	2	6002398	B1	1999-12-14	Wilson, Daniel	Entire Document
	3	6014135	B1	2000-01-11	Fernandes, Antonio	Entire Document
	4	6052524	B1	2000-04-18	Pauna, Mark	Entire Document
	5	6104325	B1	2000-08-15	Liaw, et al.	Entire Document
	6	6121965	B1	2000-09-19	Kenney, et al.	Entire Document
	7	6223144	B1	2001-04-24	Barnett, et al.	Entire Document
	8	6282551	B1	2001-08-28	Anderson, et al.	Entire Document

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9	6304101	B1	2001-10-16	Nishihara, Yoshio	Entire Document
10	6384947	B1	2002-05-07	Ackerman, et al.	Entire Document
11	6438565	B1	2002-08-20	Ammirato, et al.	Entire Document
12	6456304	B1	2002-09-24	Angiulo, et al.	Entire Document
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20	5883623	B1	1999-03-16	Cseri, Istvan	Entire Document
21	5874958	B1	1999-02-23	Ludolph, Frank	Entire Document
22	4670838	B1	1987-06-02	Kawata	Entire Document
23	4939637	B1	1990-07-03	Pawloski	Entire Document
24	4969087	B1	1990-11-06	Tanagawa, et al.	Entire Document
25	5313618	B1	1994-05-17	Pawloski	Entire Document
26	5371878	B1	1994-12-06	Coker	Entire Document
27	5495593	B1	1996-02-27	Elmer, et al.	Entire Document
28	5574852	B1	1996-11-12	Bakker, et al.	Entire Document
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31	5416895	B1	1995-05-16	Anderson, et al.	Entire Document
32	6072803	B1	2000-06-06	Allmond, et al.	Entire Document
33	6278568	B1	2001-08-21	Cloke, et al.	Entire Document
34	6371878	B1	2004-04-16	Bowen	Entire Document
35	7206733	B1	2007-04-17	Nemecek	Entire Document
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37	6631508	B1	2003-10-07	Williams	Entire Document
38	6658633	B1	2003-12-02	Devins, et al.	Entire Document
39	3378877	B1	2004-01-13	Perry, et al.	Entire Document
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42	5784545	B1	1998-07-21	Anderson, et al.	Entire Document
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33	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems, Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>
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Application Number		10033027
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First Named Inventor	Warren Snyder	
Art Unit	2183	
Examiner Name	Idriss N. Alrobaye	
Attorney Docket Number	CD00232M	

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Examiner Signature	/Idriss Alrobaye/	Date Considered	08/22/2009
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33	6003054	B1	1999-12-14	Oshima et al.	Entire Document
34	4250464	B1	1981-02-10	Schade, Jr.	Entire Document
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Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

Examiner Initial*	Cite No	Publication Number	Kind Code	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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29	USPTO Notice of Allowance for Application Number 10/113,064 (CD01200M) dated 09/21/2006; 7 pages	<input type="checkbox"/>
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	2	6453175	B1	2002-09-17	Mizell et al.	Entire Document
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37	6567932	B1	2003-05-20	Edwards et al.	Entire Document
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39	6601189	B1	2003-07-29	Edwards et al.	Entire Document
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( Not for submission under 37 CFR 1.99)

Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

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USPTO Non-Final Rejection for Application Number 09/887,923 (CD00229) dated 05/25/2004; 7 pages



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Examiner Signature	/Idriss Alrobaye/	Date Considered	08/23/2009
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Doc code: IDS

Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (04-09)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10033027
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	Examiner Name	Idriss N. Alrobaye	
	Attorney Docket Number	CD00232M	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20040153802	A1	2004-08-05	Kudo et al.	Entire Document
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	3	20010010083	A1	2001-07-26	Satoh	Entire Document
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	5	20020065646	A1	2002-05-30	Waldie et al.	Entire Document
	6	20020133771	A1	2002-09-19	Barnett	Entire Document

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	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
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	5	USPTO Advisory Action for Application Number 09/875,599 (CD00180) dated 06/08/2005; 3 pages	<input type="checkbox"/>
	6	USPTO Final Rejection for Application Number 09/875,599 (CD00180) dated 03/29/2005; 20 pages	<input type="checkbox"/>
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24	USPTO Non-Final Rejection for Application Number 09/887,955 (CD00197) dated 05/26/2004; 5 pages	<input type="checkbox"/>
25	USPTO Notice of Allowance for Application Number 09/826,397 (CD00190) dated 10/07/2004; 8 pages	<input type="checkbox"/>
26	USPTO Non-Final Rejection for Application Number 09/826,397 (CD00190) dated 04/21/2004; 6 pages	<input type="checkbox"/>
27	USPTO Notice of Allowance for Application Number 09/893,048 (CD00231) dated 07/25/2006; 4 pages	<input type="checkbox"/>
28	USPTO Non-Final Rejection for Application Number 09/893,048 (CD00231) dated 01/12/2006; 6 pages	<input type="checkbox"/>
29	USPTO Non-Final Rejection for Application Number 09/893,048 (CD00231) dated 07/27/2005; 6 pages	<input type="checkbox"/>

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44	USPTO Notice of Allowance for Application Number 09/923,461 (CD00192) dated 05/12/2005; 6 pages	<input type="checkbox"/>
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**U.S. PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20020121679	A1	2002-10-29	Bazarjani et al.	Entire Document
	2	20040054821	A1	2004-03-18	Warren et al.	Entire Document

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	1	USPTO Notice of Allowance for Application Number 10/011,214 (CD00191) dated 04/11/2005; 4 pages	<input type="checkbox"/>
	2	USPTO Final Rejection for Application Number 10/011,214 (CD00191) dated 01/21/2005; 8 pages	<input type="checkbox"/>
	3	USPTO Non-Final Rejection for Application Number 10/011,214 (CD00191) dated 08/13/2004; 10 pages	<input type="checkbox"/>
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8	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE; 6 pages	<input type="checkbox"/>
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38	Application Number 09/275,336 (CD98087): "Programmable Oscillator Scheme;" Mar et al.; 25 pages	<input type="checkbox"/>
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	2	5594388	B1	1997-01-14	O'Shaughnessy et al.	Entire Document
	3	5963105	B1	1999-10-05	Nguyen	Entire Document
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10	7250825	B1	2007-07-31	Wilson et al.	Entire Document
11	7200507	B1	2007-04-03	Chen et al.	Entire Document
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16	6154064	B1	2000-11-28	Proebsting, Robert J.	Entire Document
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18	4326135	B1	1982-04-20	Jarrett et al.	Entire Document
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43	6026134	B1	2000-02-15	Duffy et al.	Entire Document
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17	USPTO Non-Final Rejection for Application Number 11/132,894 (CD04064) dated 12/19/2006; 12 pages	<input type="checkbox"/>
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	3	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 08/28/2003; 9 pages	<input type="checkbox"/>
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12	Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al.; 32 pages	<input type="checkbox"/>
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6	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/21/2008; 12 pages	<input type="checkbox"/>
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28	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 08/26/2004; 7 pages	<input type="checkbox"/>
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Attorney Docket Number	CD00232M

39	PCT International Search Report of the International Searching Authority for PCT/US05/28793, filed 08/12/2005, mailed 11/19/2007; 5 pages	<input type="checkbox"/>
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42	Efstathiou, "Analog Electronics: Basic Circuits of Operational Amplifiers," <http://web.archive.org/web/20021231045232> December 31, 2002, version, retrieved from the Internet Archives; 10 pages	<input type="checkbox"/>
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10033027
	Filing Date		2001-10-22
	First Named Inventor	Warren Snyder	
	Art Unit	2183	
	Examiner Name	Idriss N. Alrobaye	
	Attorney Docket Number	CD00232M	

**U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5708798	B1	1998-01-13	Lynch et al.	Entire Document
	2	6825869	B1	2004-11-30	Bang	Entire Document
	3	4942540	B1	1990-07-17	Black et al.	Entire Document
	4	5394522	B1	1995-02-28	Sanchez-Frank et al.	Entire Document
	5	5867399	B1	1999-02-02	Rostoker et al.	Entire Document
	6	5930150	B1	1999-07-27	Cohen et al.	Entire Document
	7	5933356	B1	1999-08-03	Rostoker et al.	Entire Document
	8	5941991	B1	1999-08-24	Kageshima	Entire Document

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9	6246410	B1	2001-06-12	Bergeron et al.	Entire Document
10	6366300	B1	2002-04-02	Ohara et al.	Entire Document
11	6388109	B1	2002-05-14	Shwarz et al.	Entire Document
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	1	Kory Hopkins, "Definition;" 01/16/1997; < <a href="http://www.cs.sfu.ca/cs/people/GradStudent.html">http://www.cs.sfu.ca/cs/people/GradStudent.html</a> >; 1 page	<input type="checkbox"/>
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	3	"The Gemini Netlist Comparison Project;" < <a href="http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html">http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html</a> > larry@cs.washington.edu; 2 pages	<input type="checkbox"/>
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